



Fig. 1 US-CFUS, USPAT, EPA, JPO ☐ Flag

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((insulates\$1 near gate near field near effect near transistor) or FET\$1) and contact and (BOX or buried near oxide) and (gate with (ring annular))

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050032291 A1	20050210	45	Methods of forming power semiconductor devices having laterally extending base shielding regions	438/212	438/268
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20050067216 A1	20050113	7	MOS ESD CDM clamp with integral substrate injection guarding and method for fabrication	333/202	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040262693 A1	20041230	36	Semiconductor storage device and semiconductor integrated circuit	257/369	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040222449 A1	20041111	29	Solid-state imaging device and driving method of the same	257/292	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20040212031 A1	20041028	18	Solid-state imaging device	257/462	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20040266991 A1	20041021	22	Solid-state imaging device	257/292	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20040202422 A1	20041014	42	Tuning the index of a waveguide structure	385/37	
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20040261047 A1	20041014	18	Solid-state imaging device	257/292	
9	<input type="checkbox"/>	<input type="checkbox"/>	US 20040166640 A1	20040826	22	Method for preparing a ring-formed body, and magnetic memory device and method for manufacturing the same	438/277	
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20040164760 A1	20040826	36	Wireless radio frequency technique design and method for testing	324/765	
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20040104446 A1	20040603	18	Semiconductor module and power conversion device	257/448	257/E23.104; 257/E23.187; 257/E25.016